


[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alerts](#) | [Purchase History](#)

Welcome United States Patent and Trademark Office

Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

Results for "((specman)-in-metadate)"

Your search matched 6 of 2485176 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance in Descending order**.

Join IEEE Now

[Learn More »](#)

Search Options

[View Session History](#)[New Search](#)

Key

IEEE JNL	IEEE Journal or Magazine
IET JNL	IET Journal or Magazine
AiP JNL	AiP Journal
AVS JNL	AVS Journal
IEEE CNF	IEEE Conference Proceeding
IET CNF	IET Conference Proceeding
IEEE STD	IEEE Standard



Indicates open access content

Modify Search

((specman)-in-metadate)

[Search](#)☐ Check to search only within this results setDisplay Format: ☒ Citation ☐ Citation & Abstract

IEEE/ET/AIP/AVS

Books

Educational Courses

A

IEEE/IET journals, transactions, letters, magazines, conference proceedings, AIP/standards.

[view selected items](#)[Select All](#) [Deselect All](#)**1. Efficient Assertion Based Verification using TLM**

Habibi, A.; Tahar, S.; Samarah, A.; Donglin Li; Ait Mohamed, O.;  
 Design, Automation and Test in Europe, 2006. DATE '06. Proceedings  
 Volume 1, 6-10 March 2006 Page(s):1 - 6  
 Digital Object Identifier 10.1109/DATE.2006.244005

[AbstractPlus](#) | [Full Text: PDF](#)(132 KB) [IEEE CNF](#)  
[Rights and Permissions](#)

**2. Verification of Ethernet IP Core MAC Design Using Design Methodology**

Assaf, M.H.; Das, S.R.; Hernias, W.; Petriu, E.M.; Biswas, S.;  
 Instrumentation and Measurement Technology Conference Proceedings, 200  
 12-15 May 2008 Page(s):1669 - 1674  
 Digital Object Identifier 10.1109/IMTC.2008.4547312

[AbstractPlus](#) | [Full Text: PDF](#)(1912 KB) [IEEE CNF](#)  
[Rights and Permissions](#)

**3. Functional verification of pipelined processors: a case study**

Prabhat Mishra; Nikil Dutt; Yaron Kishai;  
 Fibres and Optical Passive Components, 2005. Proceedings of 2005 IEEE/LE  
 9-10 Sept. 2004 Page(s):79 - 84  
 Digital Object Identifier 10.1109/MTV.2004.14

[AbstractPlus](#) | [Full Text: PDF](#)(136 KB) [IEEE CNF](#)  
[Rights and Permissions](#)

**4. A comparison of three verification techniques: direct random testing and property checking**

Bartley, M.G.; Galpin, D.; Blackmore, T.;

Design Automation Conference, 2002. Proceedings. 39th  
10-14 June 2002 Page(s):819 - 823  
Digital Object Identifier 10.1109/DAC.2002.1012735  
AbstractPlus | Full Text: PDF(627 KB) IEEE CNF  
Rights and Permissions



## 5. Promising Complex ASIC Design Verification Method

Assaf, M.H.; Das, S.R.; Hernias, W.; Jone, W.B.;  
Instrumentation and Measurement Technology Conference Proceedings, 200  
1-3 May 2007 Page(s):1 - 6  
Digital Object Identifier 10.1109/IMTC.2007.379056  
AbstractPlus | Full Text: PDF(2652 KB) IEEE CNF  
Rights and Permissions



## 6. A reusable pseudo-random verification environment designs: The SpaceWire interface case study

Saponara, Sergio; Vitullo, Francesco; Petri, Esa; Fanucci, Luca;  
Intelligent Data Acquisition and Advanced Computing Systems: Technology a  
IDAACS 2009. IEEE International Workshop on  
21-23 Sept. 2009 Page(s):102 - 106  
Digital Object Identifier 10.1109/IDAACS.2009.5343017  
AbstractPlus | Full Text: PDF(523 KB) IEEE CNF  
Rights and Permissions

Indexed by



[Help](#) [Contact Us](#)

© Copyright 2009